

COMMUNICATION AVAILABLE : CH-a, CH-b, · · · CH-k COMMUNICATION UNAVAILABLE : CH-k+1,CH-k+2

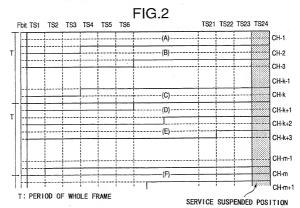
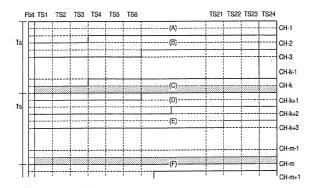
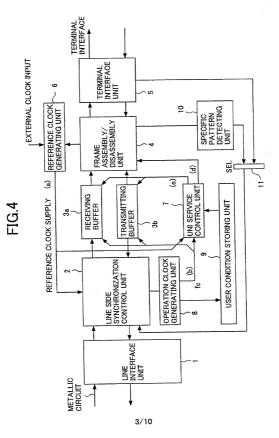
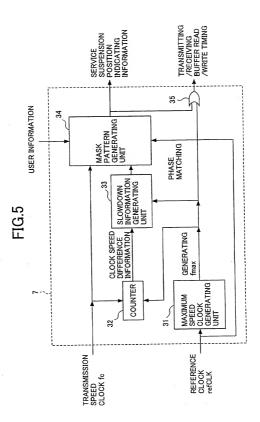


FIG.3

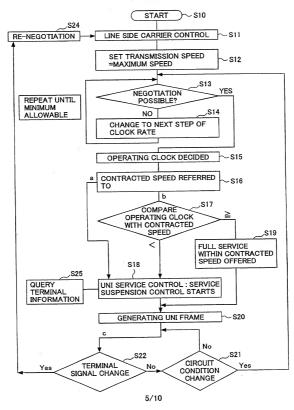


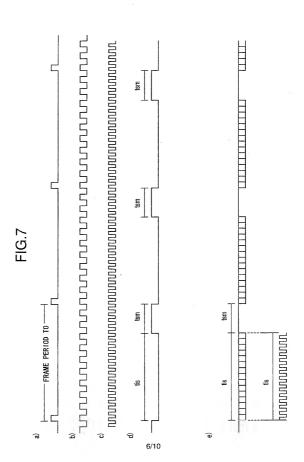
Ts: SERVICE SUSPENSION BIT SETTING PERIOD











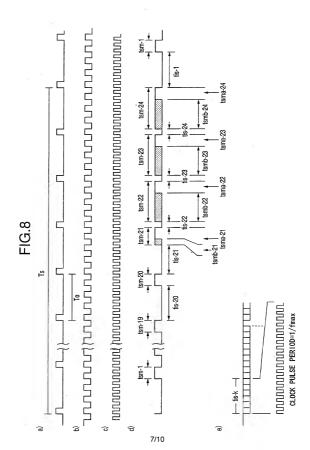


FIG.9

a) WHEN OPERATING AT MAXIMUM SPEED

Fbit								
1	TS1	TS2	TS3	TS4	•••	TS22	TS23	TS24

b) AT FALL-BACK

Fbit								
T	TS1	TS2	TS3	TS4	$[\cdots]$	TS22	TS23	TS24

FIG.10

FRAME NUMBER

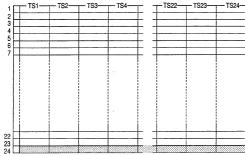


FIG.11

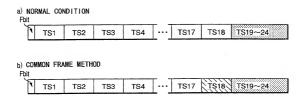


FIG.12

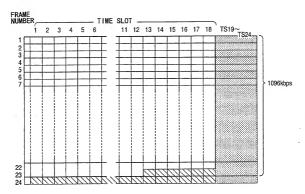


FIG.13

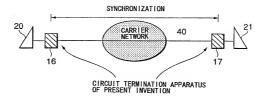


FIG.14

